

Fig. 1

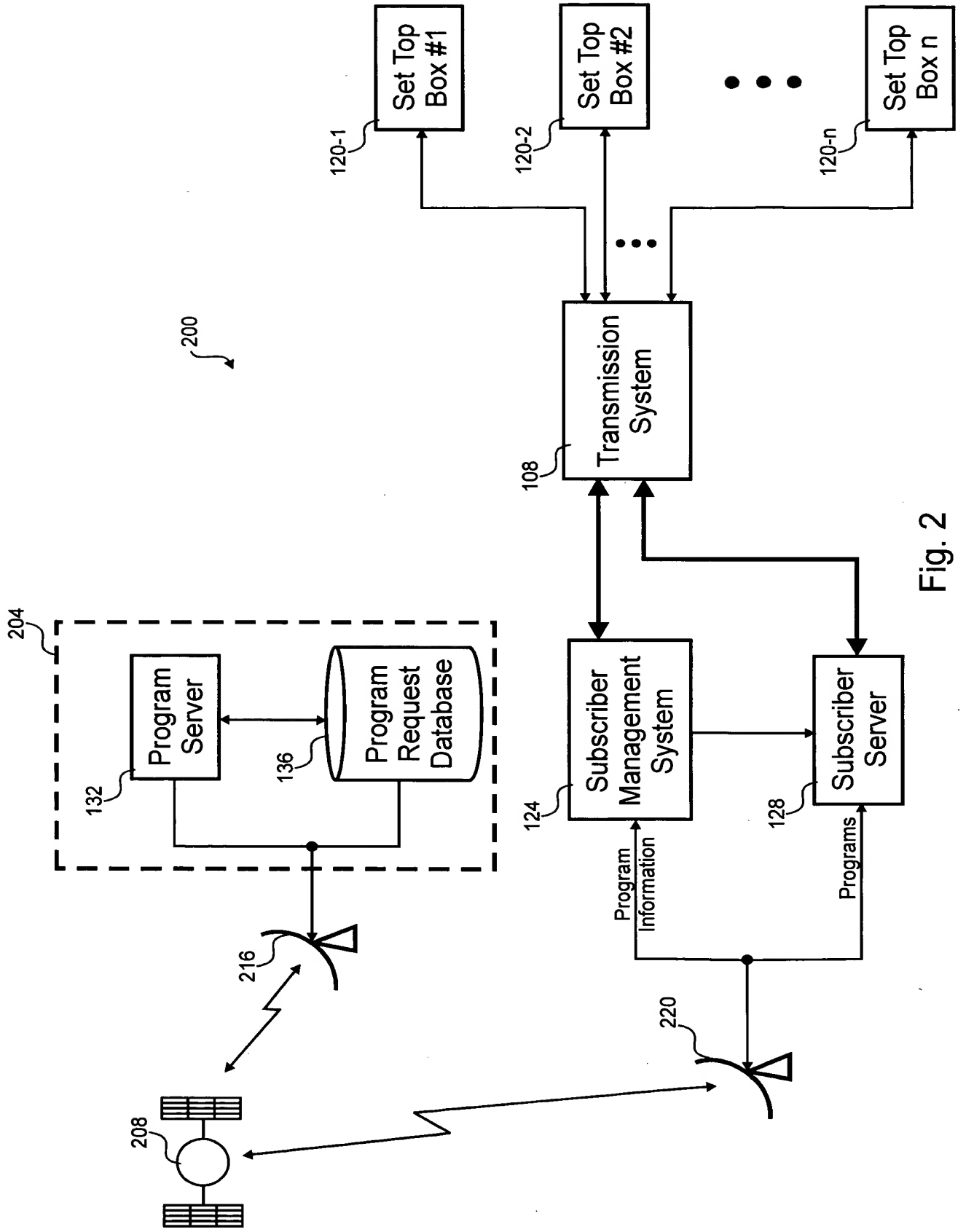


Fig. 2

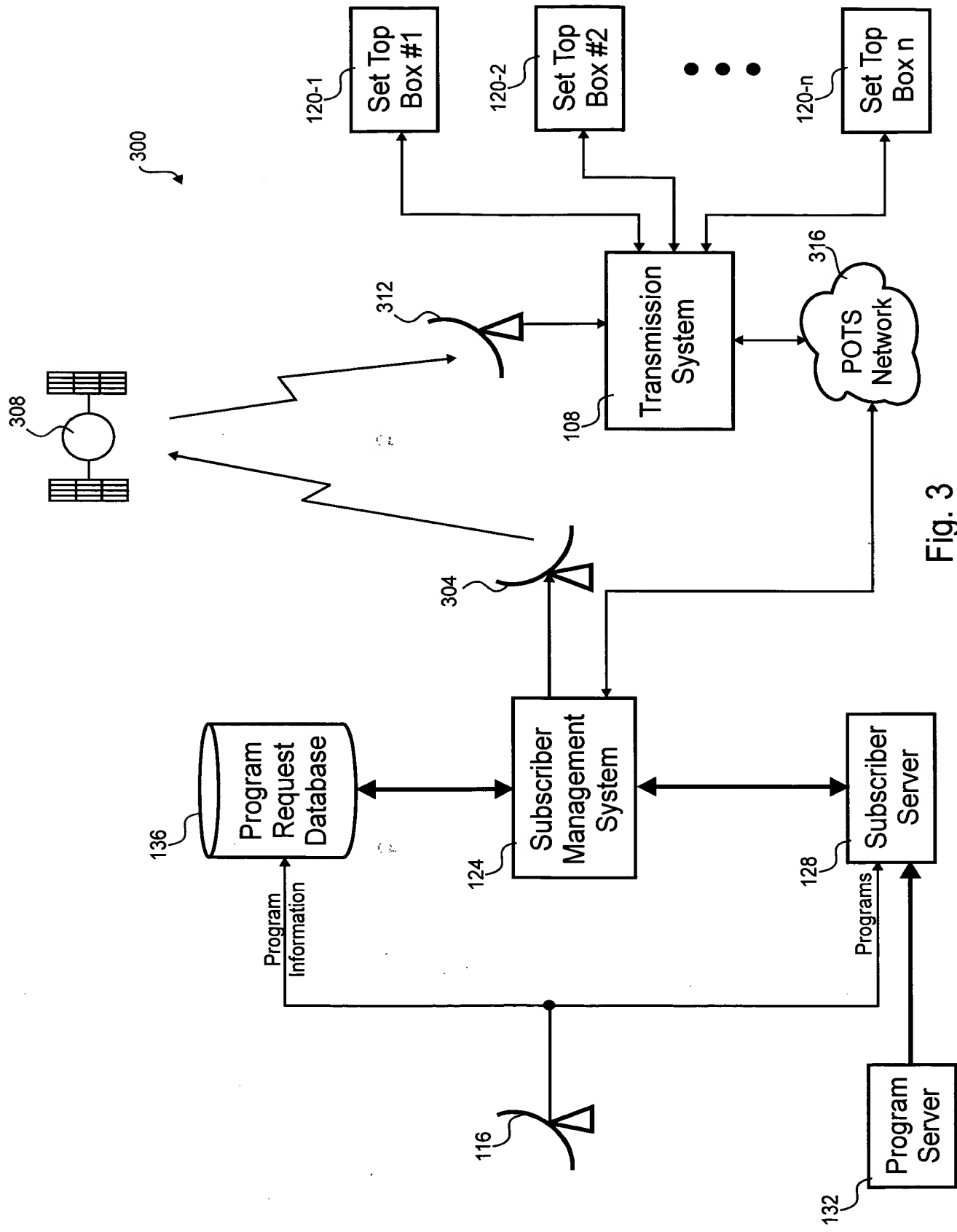


Fig. 3

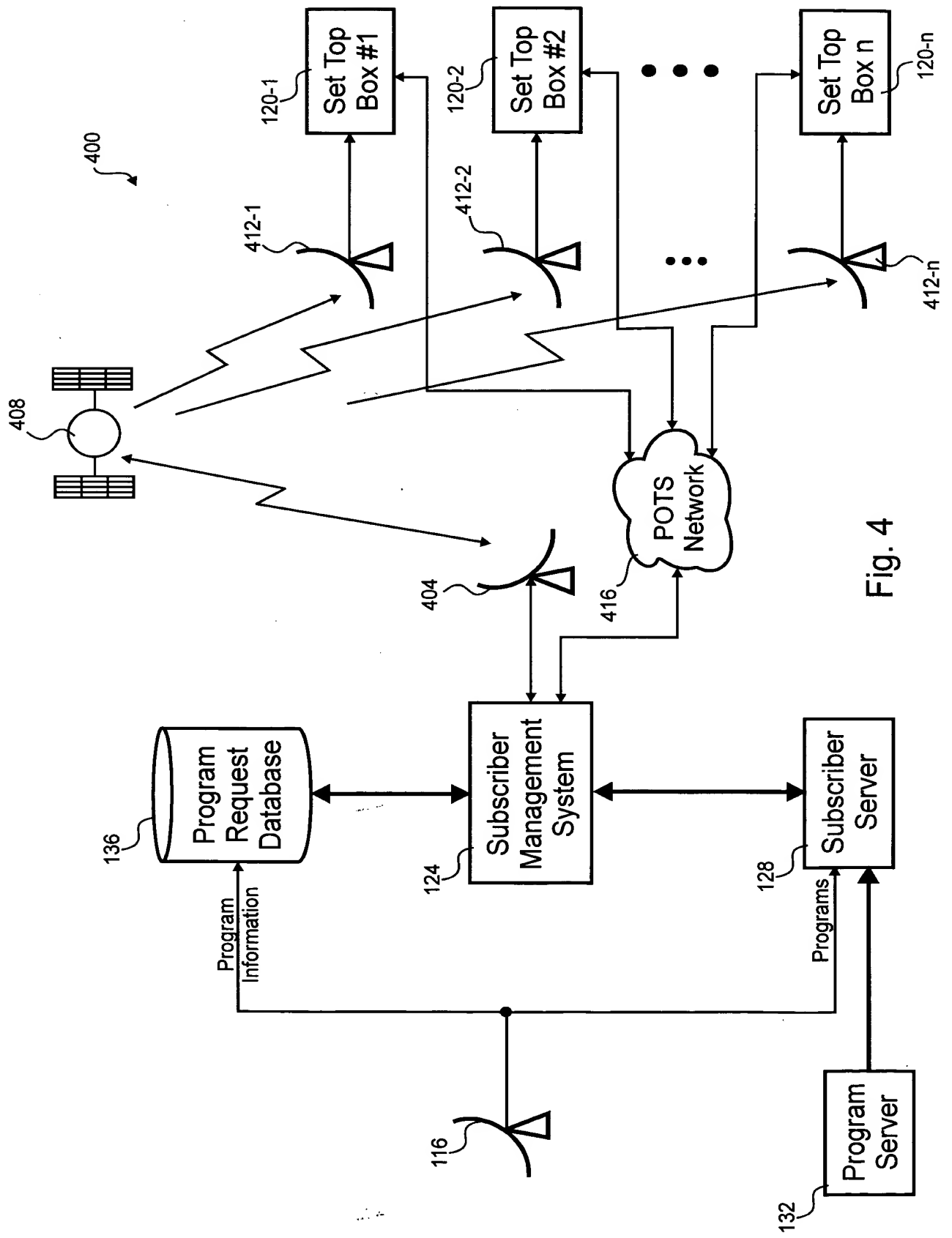


Fig. 4

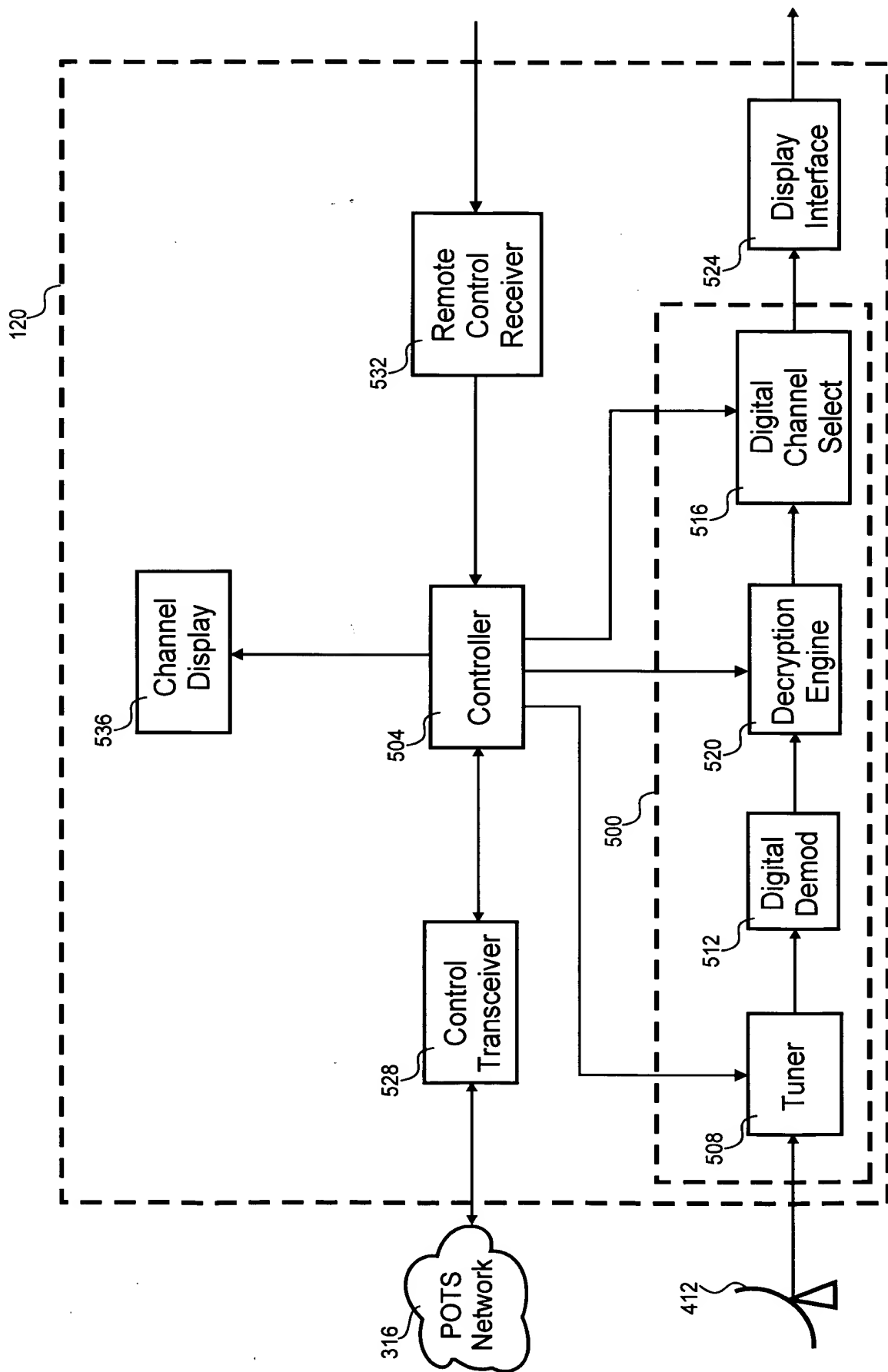


Fig. 5

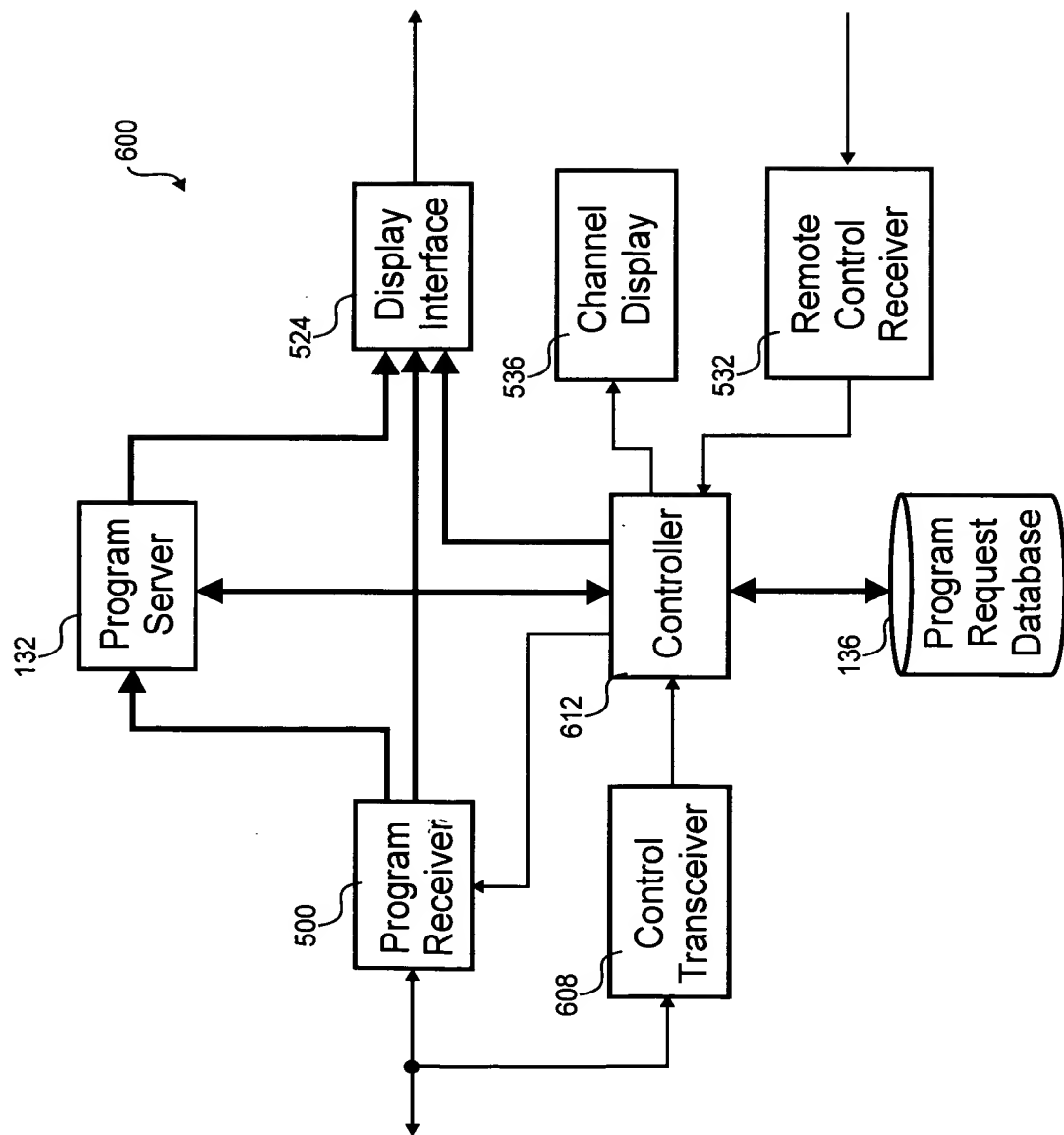


Fig. 6

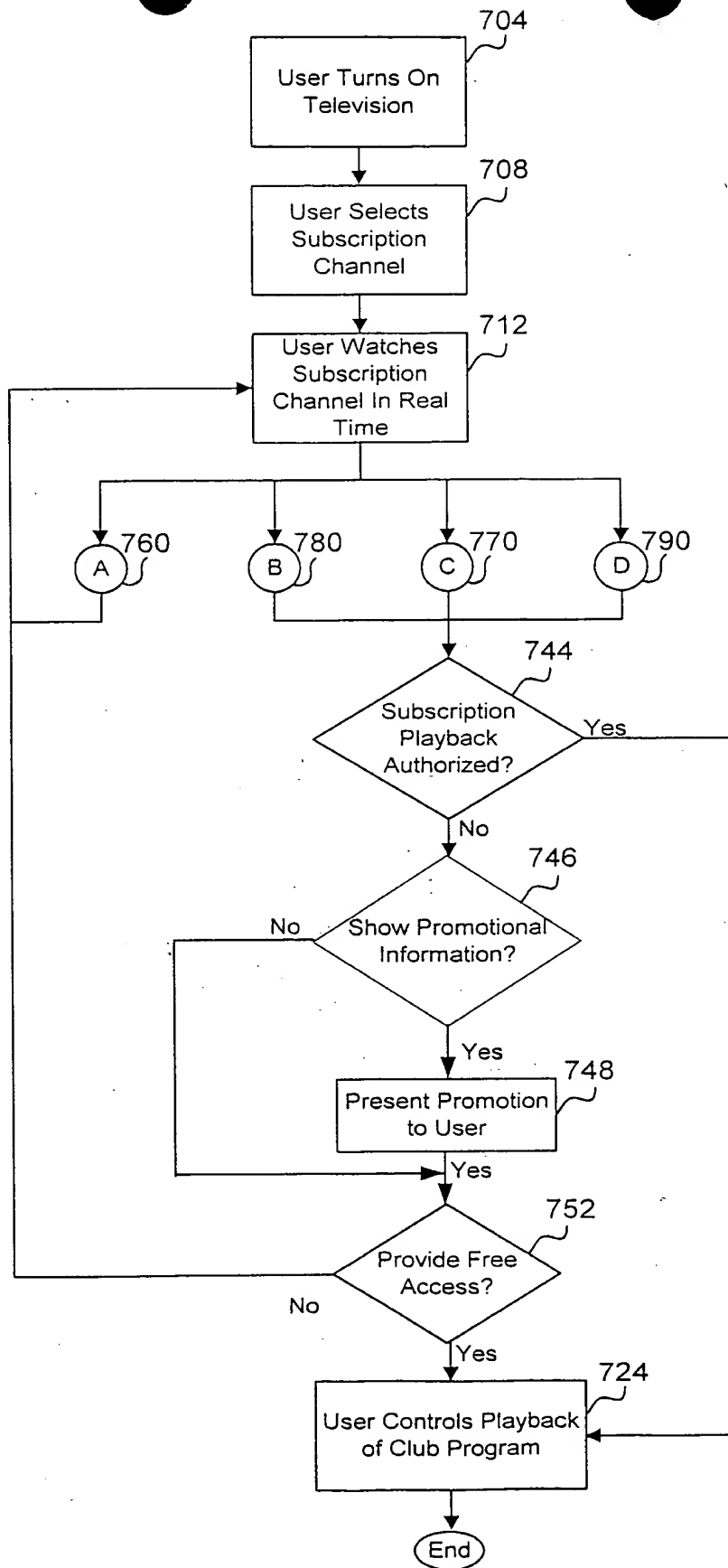


FIG. 7

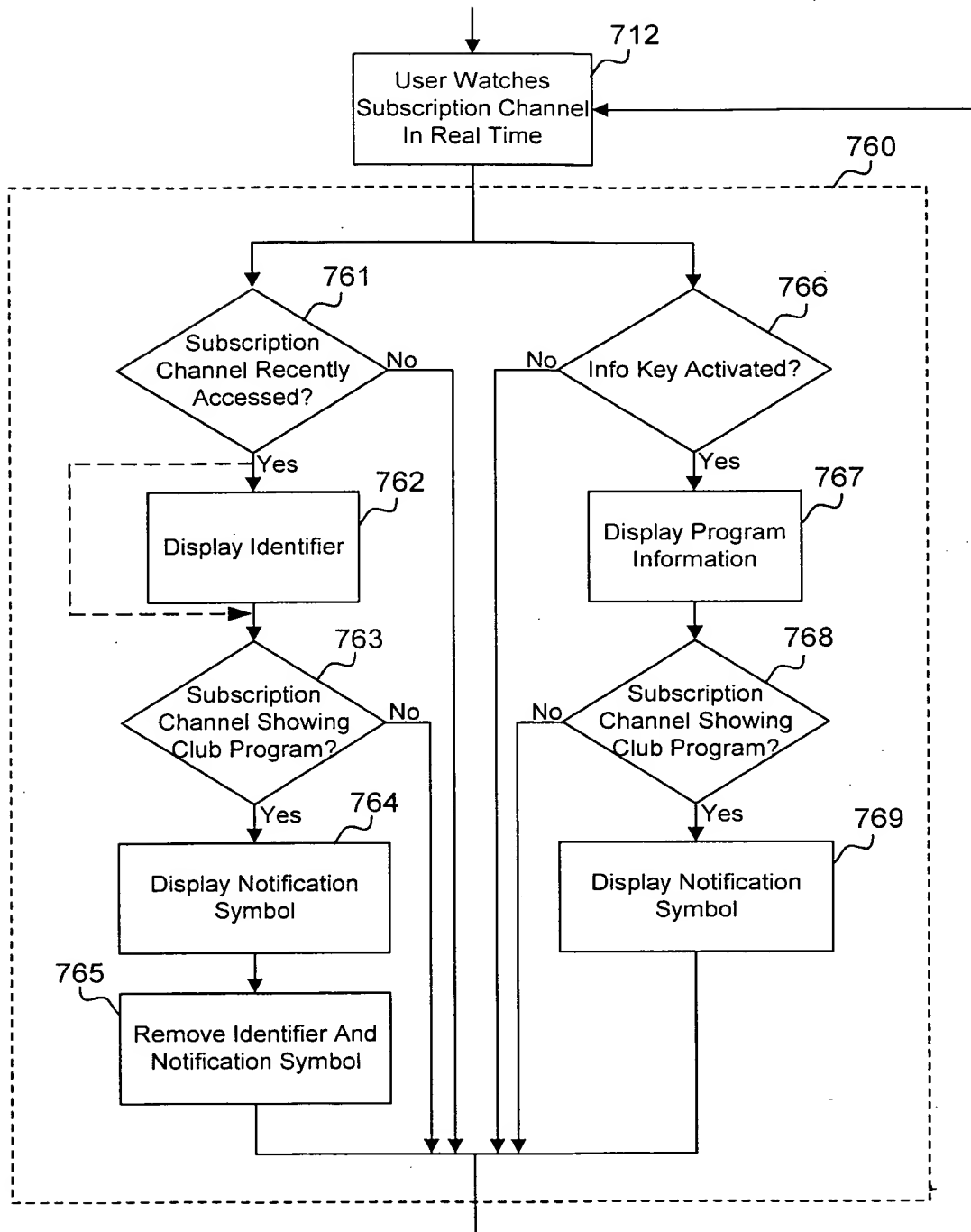


FIG. 7A

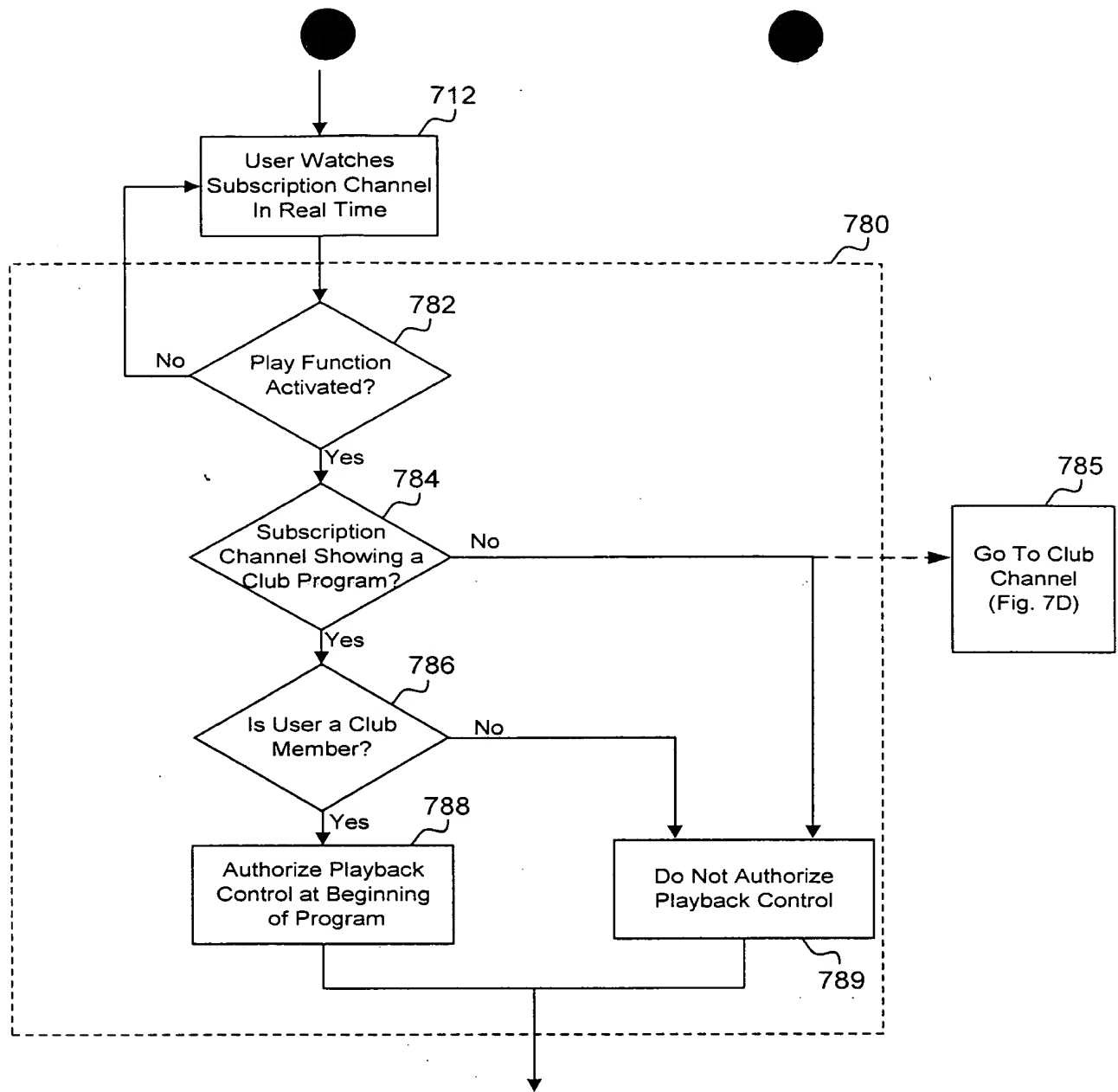


FIG. 7B

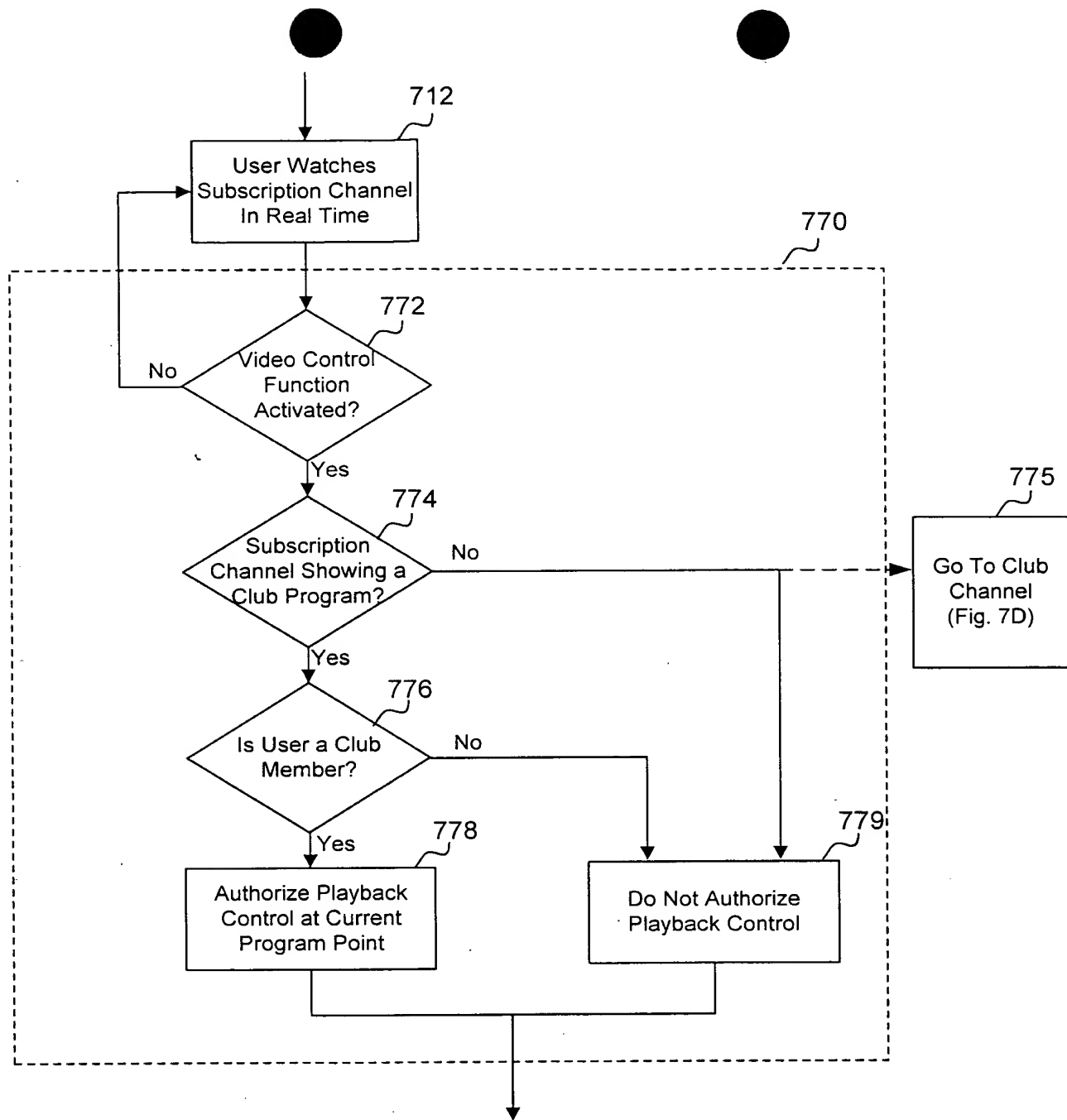


FIG. 7C

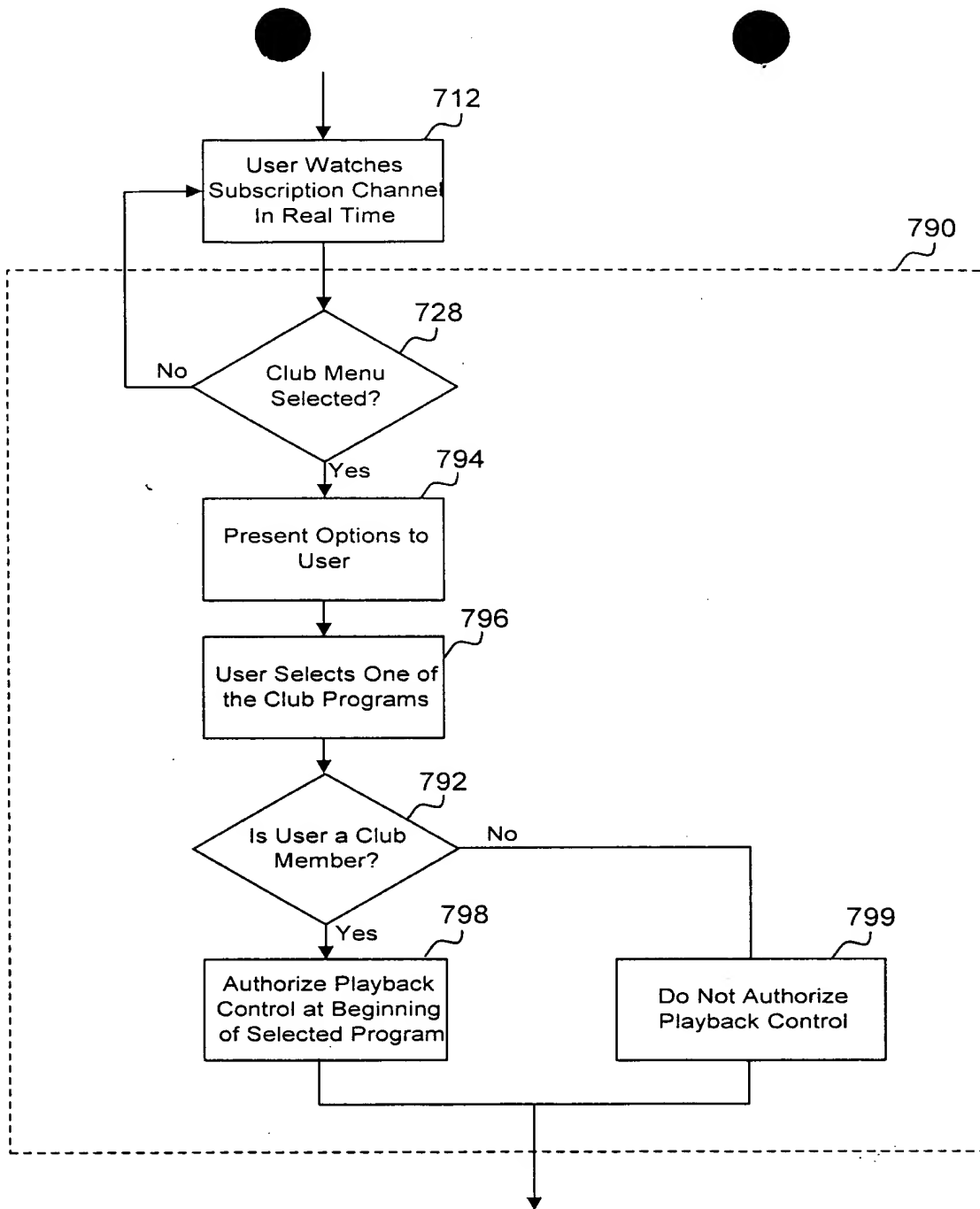


FIG. 7D

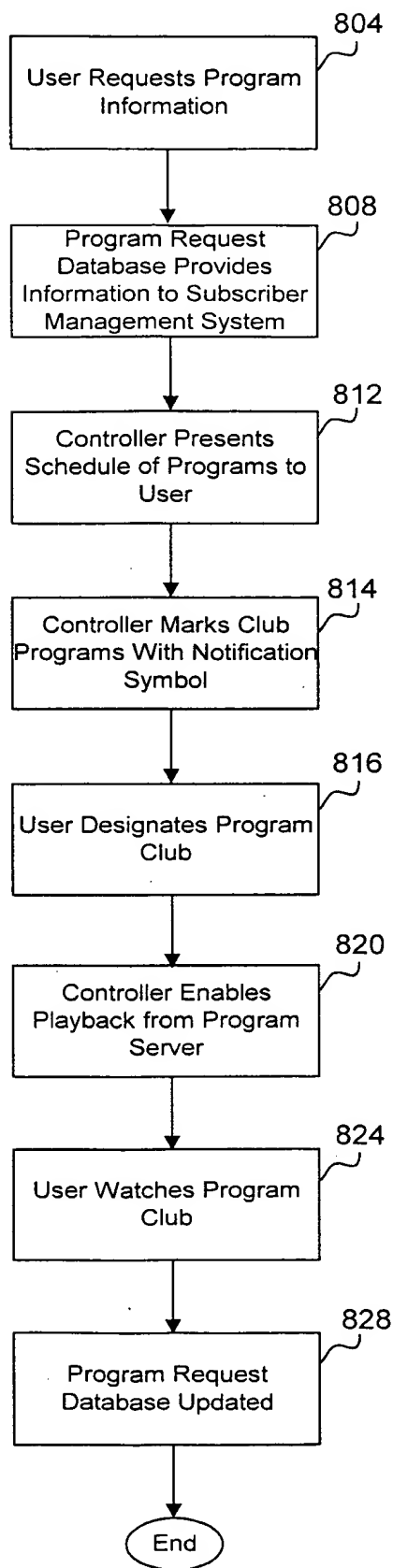


FIG. 8A

	6:00	6:30	7:00	7:30	8:00	8:30	9:00	9:30	10:00	10:30	11:00
Ch. A	840-1	840-2		☆	840-3	☆	840-1	840-4		☆	840-1
Ch. B	840-5		840-4		840-6		840-7		840-8		840-9
Ch. C	840-7		☆	840-10		840-11	840-12	840-13		840-14	
Ch. D	840-8	840-9		840-10		840-11		840-12		840-13	
Ch. E	840-11		840-12		840-13		840-14		840-15		840-16
Ch. F	840-3	840-4		840-5		840-6		840-7		840-8	
Ch. G	840-12		840-13		840-14		840-15		840-16		840-17

FIG. 8B

FIG. 9A is a schematic diagram of a time slot allocation for a viewed channel and four channels (Ch. I, Ch. II, Ch. III, Ch. IV) over a time period from 4:00 to 10:00. The diagram shows the allocation of time slots to various programs, identified by their call numbers (e.g., 904, 908, 912-1, 912-2, 912-3, 916-2, 916-3, 916-4, 920-1, 920-2, 920-3, 920-4, 924-1, 924-2, 924-3).

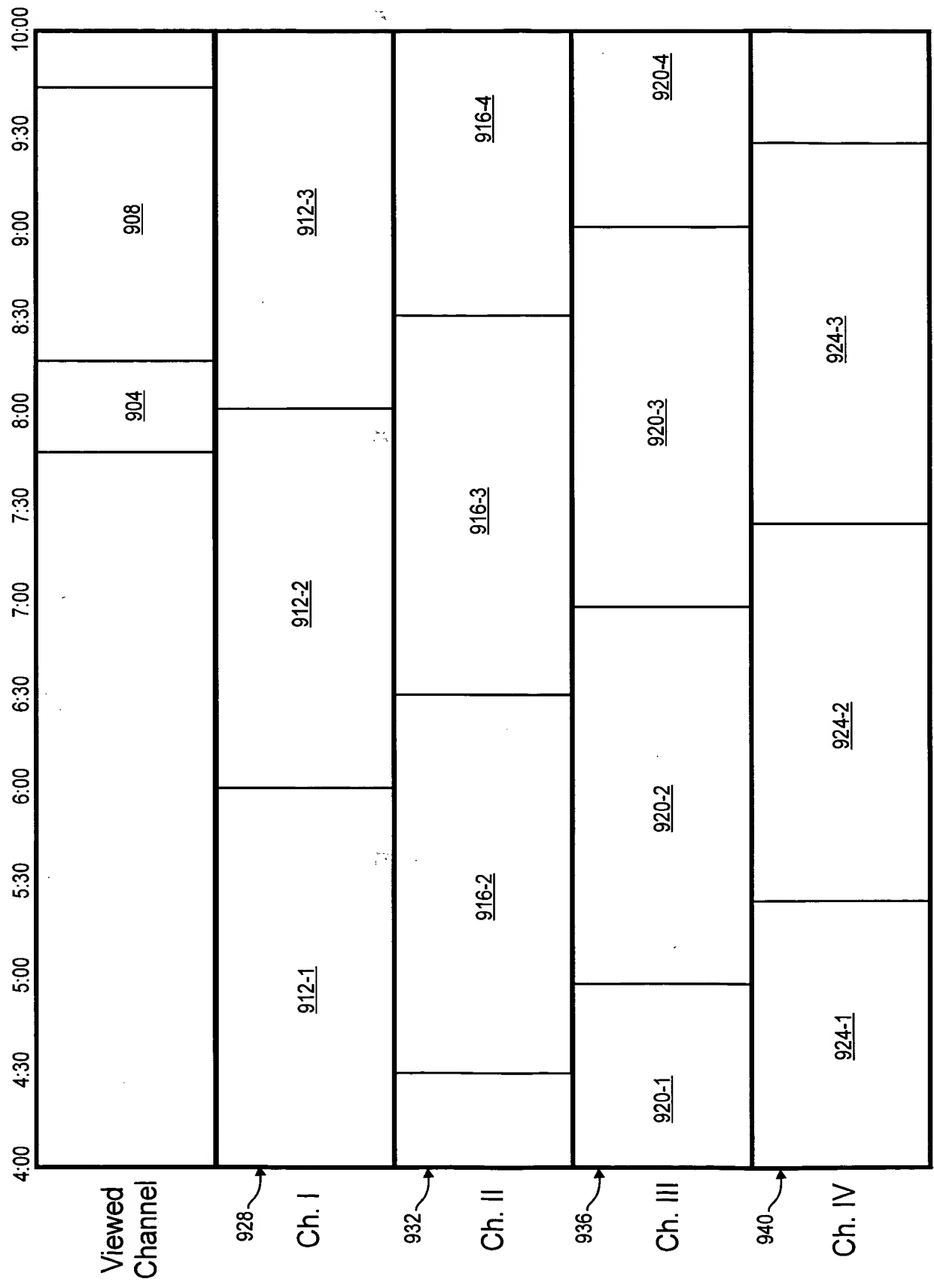
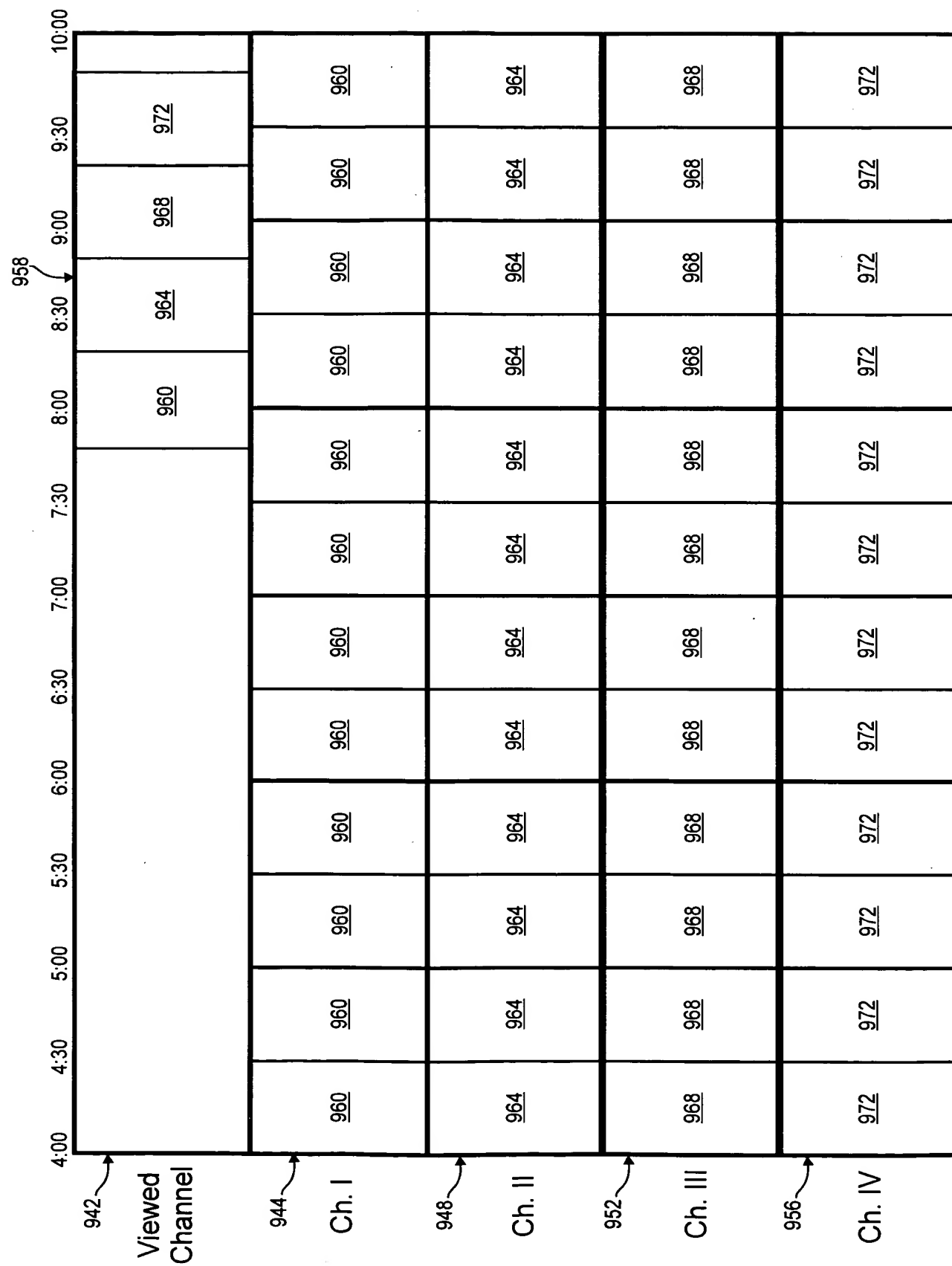


Fig. 9A



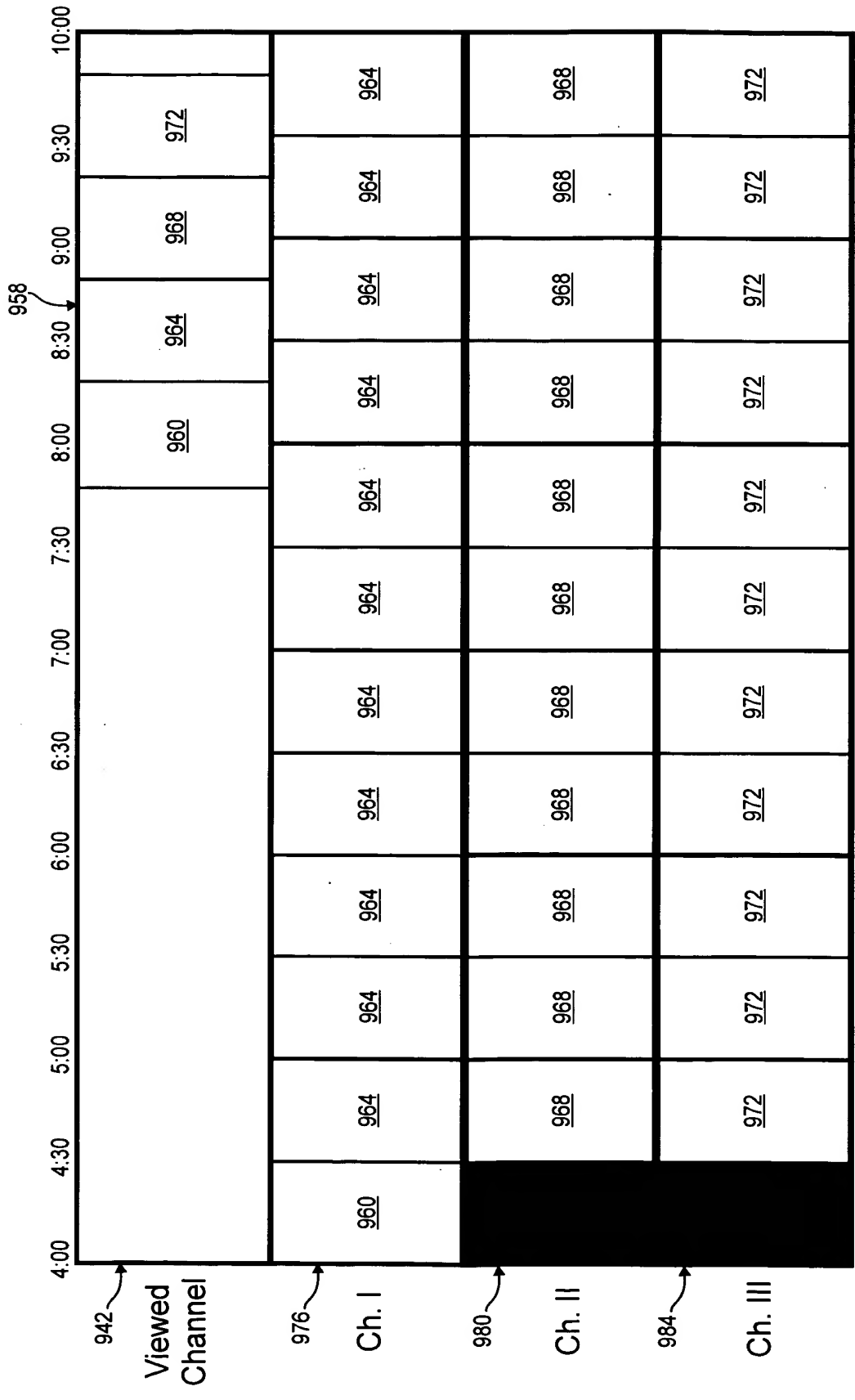


Fig. 9C

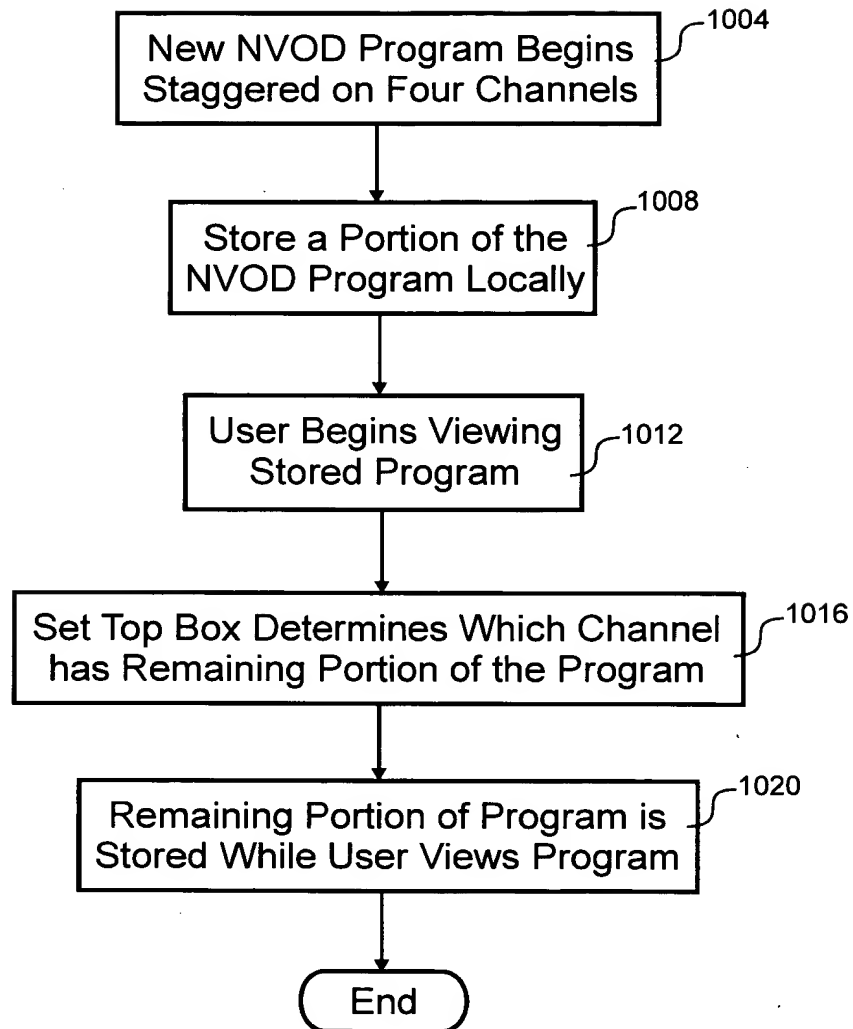


Fig. 10A

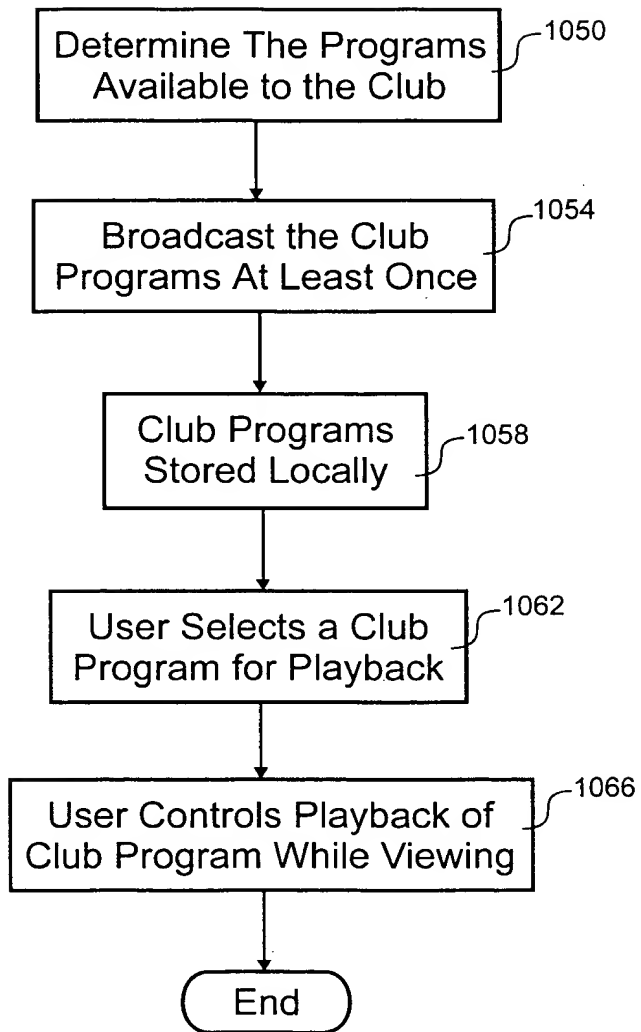


Fig. 10B

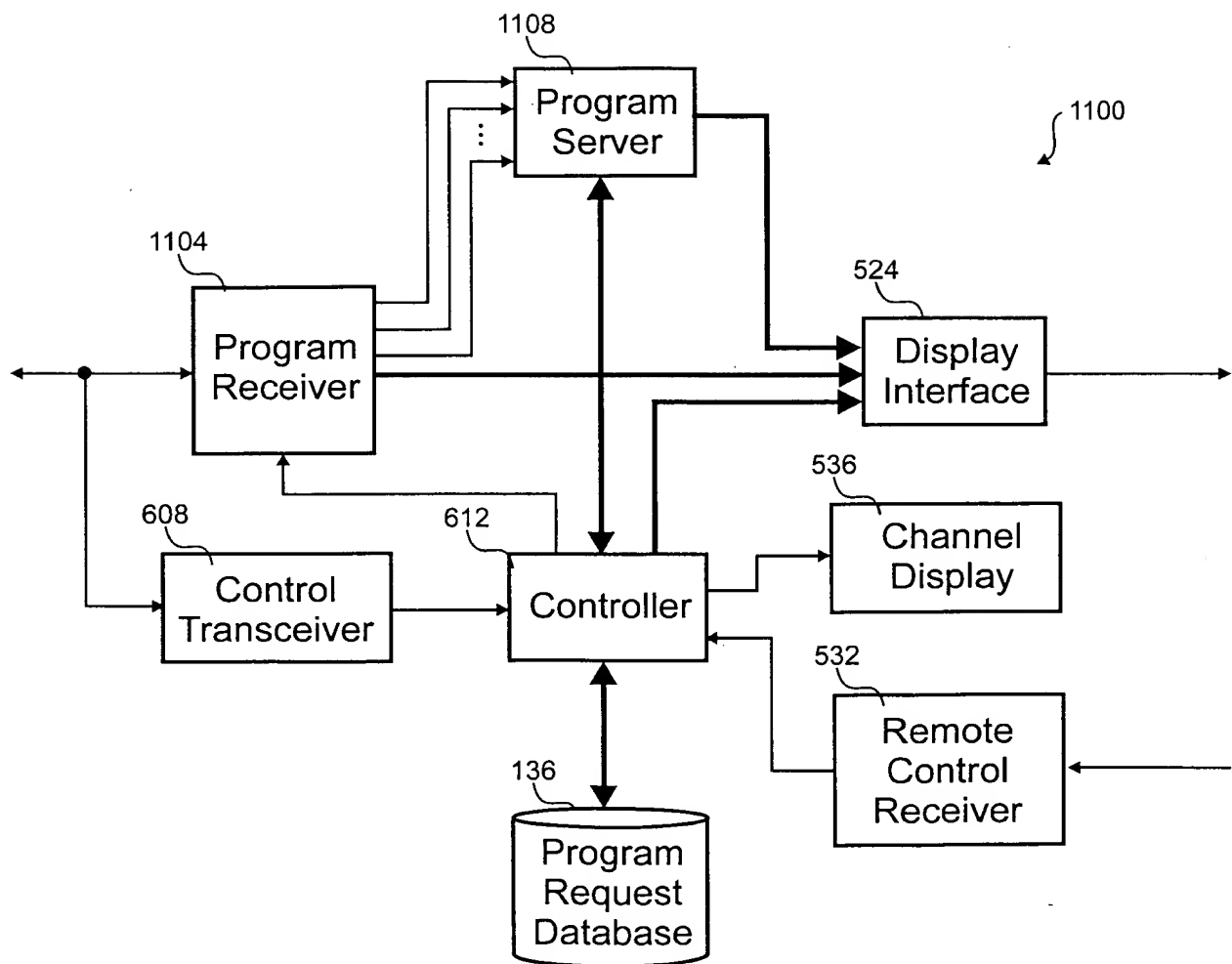


Fig. 11

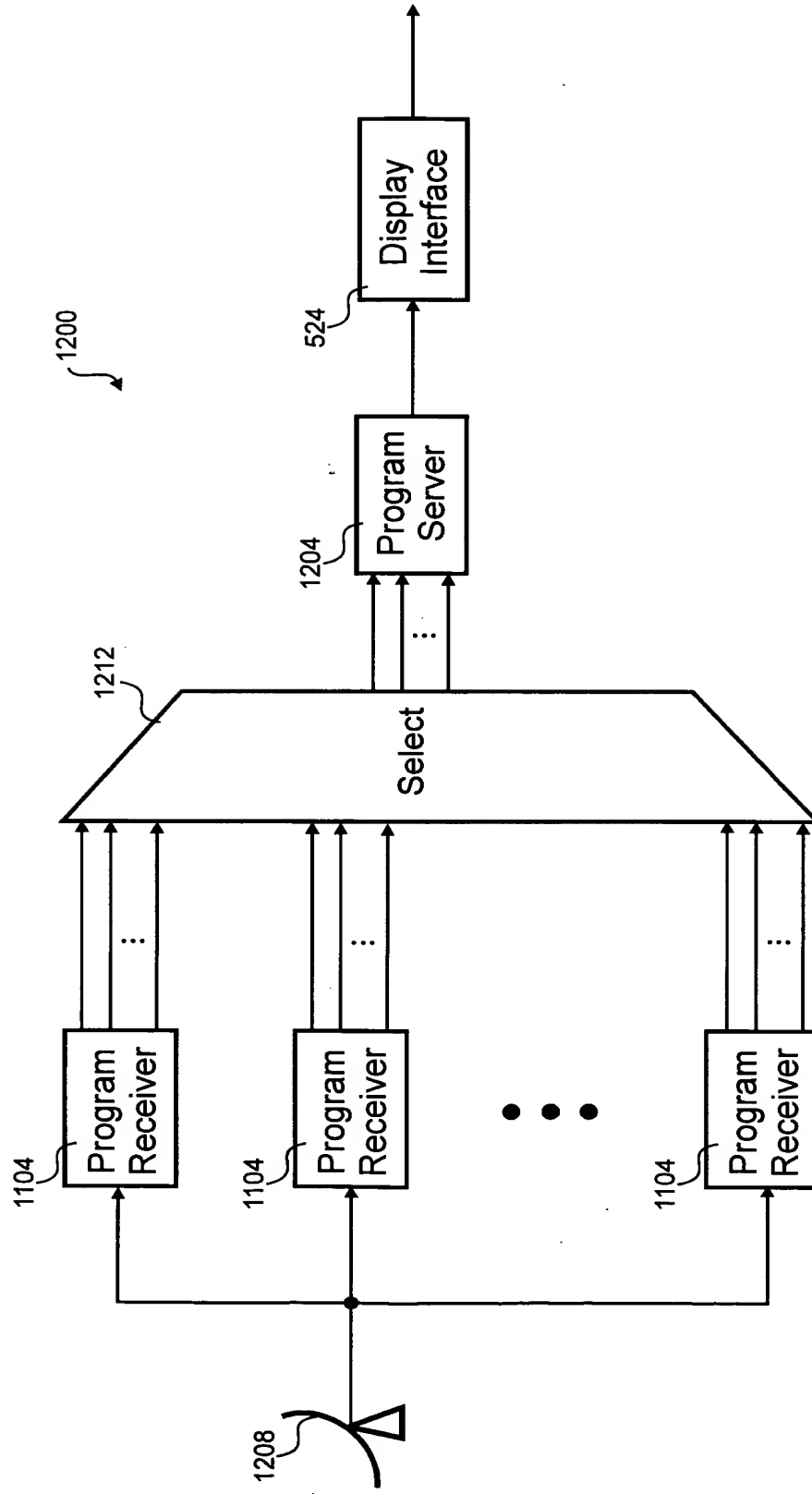


Fig. 12A

FIG. 12B is a block diagram of a system 1216.

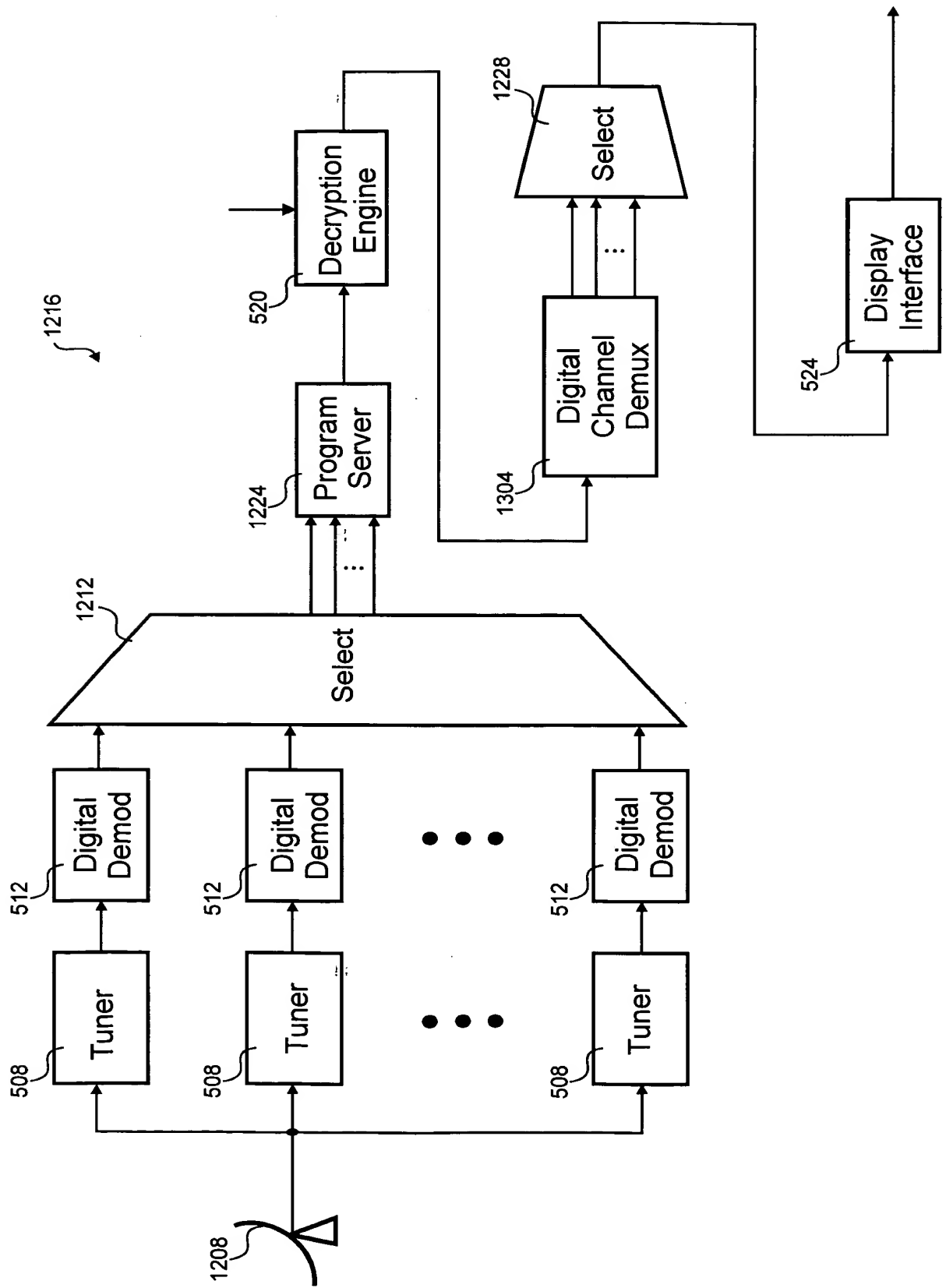


Fig. 12B

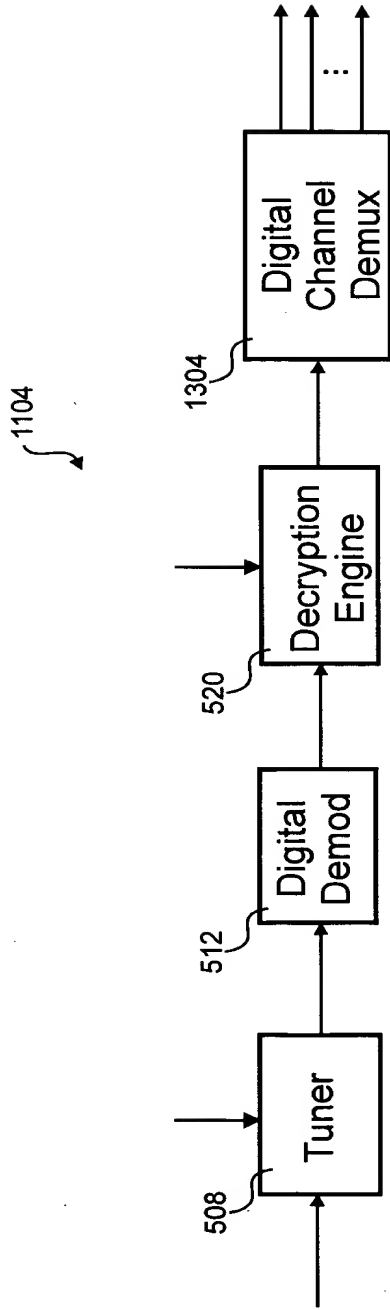


Fig. 13A

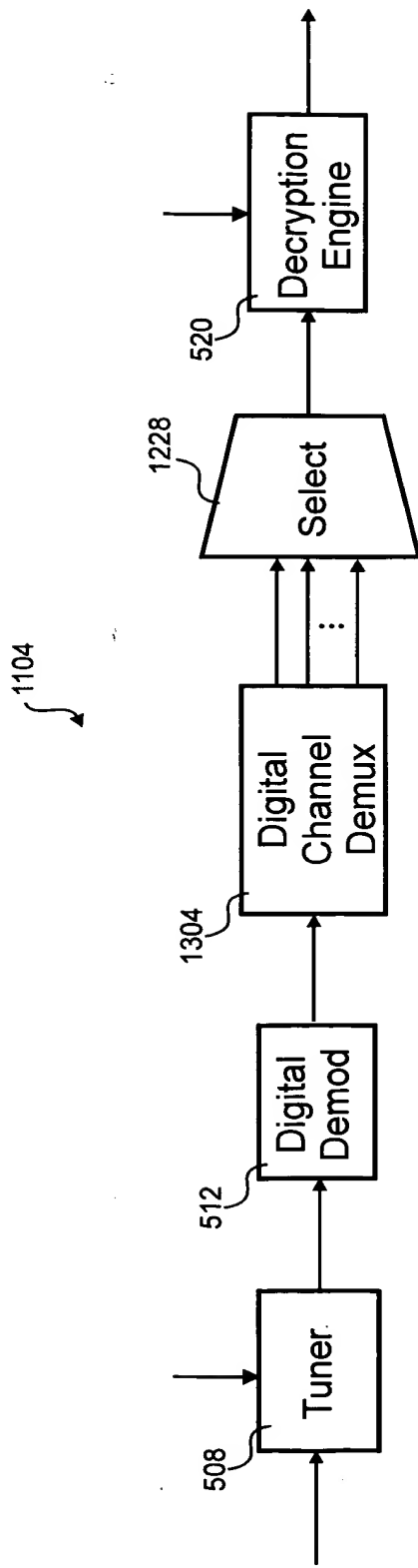


Fig. 13B

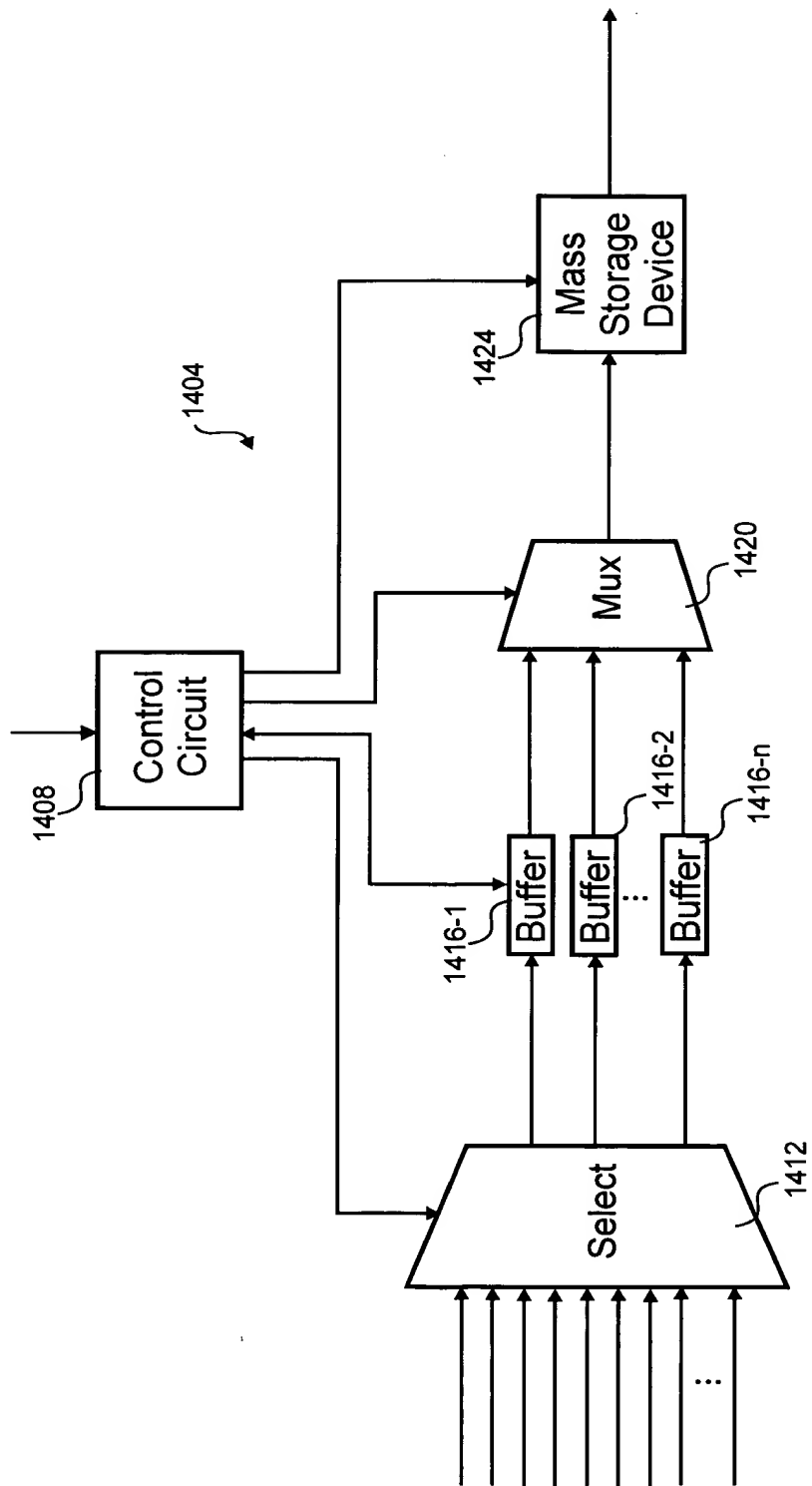


Fig. 14

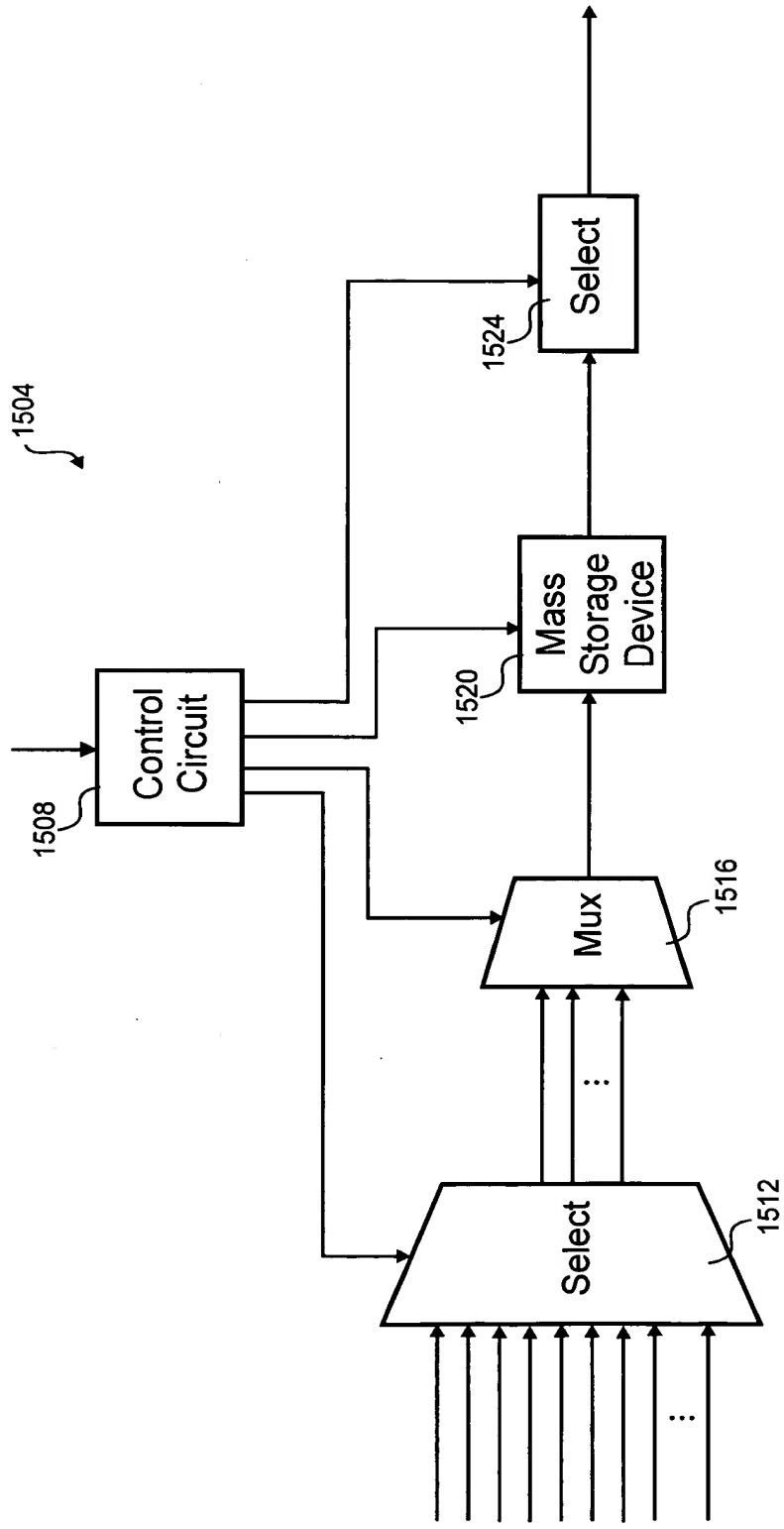


Fig. 15

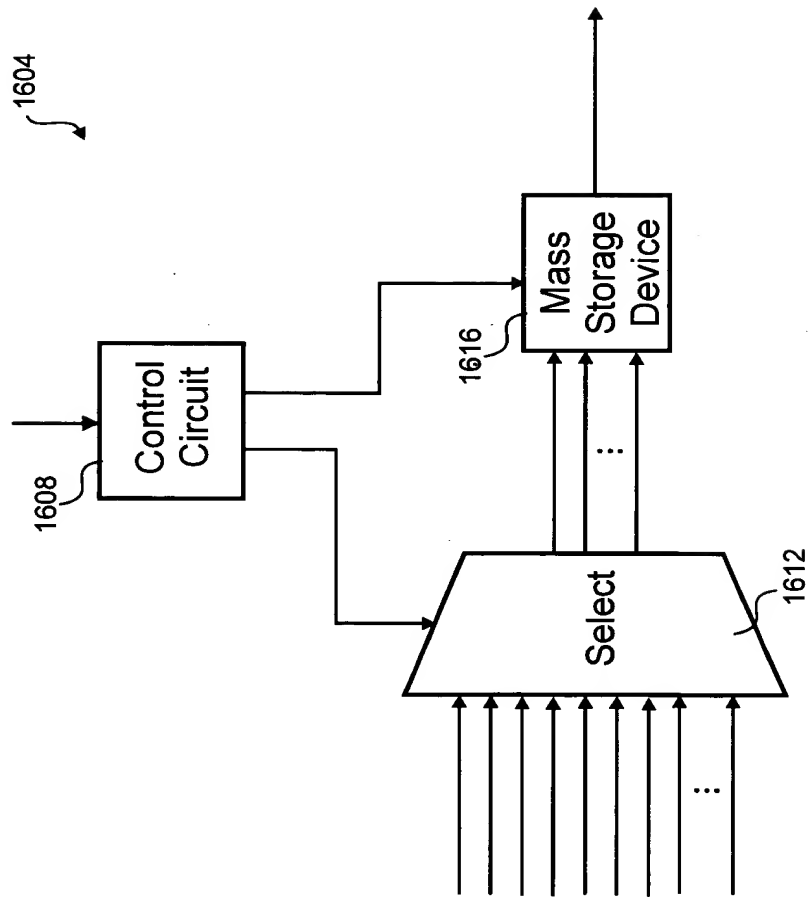


Fig. 16